Call for Papers

Special Issue of
International Journal of Computer Systems Science & Engineering
on

ATM Switching

Papers are solicited for a special issue of the International Journal of Computer Systems Science & Engineering on Asynchronous Transfer Mode (ATM) Switching to be published in the third quarter of 1996.

During the past decade, a considerable amount of effort has been made in studying and designing ATM switches which is believed to be the most developed aspect of ATM. The field has now become a mature area and ATM switches are becoming commercially available. This special issue will include a set of original and survey articles from both industry and academia that represents the current state-of-the-art in ATM switching. Possible topics include (but are not limited to):

- Switch architectures
- Buffering schemes
- Performance modeling
- Buffer management
- Commercial switches
- Fault tolerance
- Congestion control and traffic management
- Practical experience & field trials
- Simulation techniques for large switches

Five copies of complete manuscripts (not to exceed 25 double-spaced pages) should be sent to **Mohammed Atiquzzaman by 1 February 1996**. Please include a title page containing author(s) names and affiliations, postal addresses, e-mail addresses, telephone numbers, and fax numbers. Electronic (PostScript only) submissions are encouraged. Authors should follow the IJCSSE manuscript submission format.

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Important Dates:

Deadline for receipt of manuscripts:	1 February 1996
Notification of acceptance/rejection:	30 April 1996

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